## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Satoshi AIDA, et al.				GAU:		
SERIAL NO: New Application				EXAMINER:		
FILED: Herewith						
FOR:	SEMICONDUCTOR DEV	'ICE				
REQUEST FOR PRIORITY						
COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313						
nrovisio	nefit of the filing date of U.Sons of 35 U.S.C. §120.			, filed	, is claimed pursuant to the	
Full benefit of the filing date(s) of U.S. Provisional Application(s) is claimed pursuant to the provisions of 35 U.S.C. §119(e):  Application No.  Date Filed						
Applicants claim any right to priority from any earlier filed applications to which they may be entitled pursuant to the provisions of 35 U.S.C. §119, as noted below.						
In the matter of the above-identified application for patent, notice is hereby given that the applicants claim as priority:						
COUNTRY Japan		<u>APPLICATION</u> 3 2003-111190	<u>NUMBER</u>	ER MONTH/DAY/YEAR April 16, 2003		
Certified copies of the corresponding Convention Application(s)						
are submitted herewith						
☐ will be submitted prior to payment of the Final Fee						
were filed in prior application Serial No. filed						
were submitted to the International Bureau in PCT Application Number Receipt of the certified copies by the International Bureau in a timely manner under PCT Rule 17.1(a) has been acknowledged as evidenced by the attached PCT/IB/304.						
☐ (A) Application Serial No.(s) were filed in prior application Serial No. filed ; and						
☐ (B) Application Serial No.(s)						
are submitted herewith						
☐ will be submitted prior to payment of the Final Fee						
			I	Respectfully Submitted,		
			(	OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.		
				S/mWSillan		
			-	Marvin J. Spivak		
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